

Digital Temperature Sensor and Thermal Watchdog with Two-Wire Interface

Features

- SMBus interface
- Programmable Trip Point (T_{OS}) and Hysteresis (T_{HYST})
- Separate open-drain output pin operates as interrupt or comparator at output
- Register readback capability
- Power up defaults permit stand-alone operation as thermostat
- Shutdown mode to minimize power consumption
- Up to 8 G751s can be connected to a single bus

Key Specifications

- Supply Voltage **3.0V to 5.5V**
- Supply Current

operating	280 μ A (typ)
	1mA (max)
shutdown	2 μ A (typ)
- Temperature Accuracy

-25°C to 100°C	$\pm 2^\circ$ C (max)
-55°C to 125°C	$\pm 3^\circ$ C (max)

Applications

- System Thermal Management
- Personal Computers
- Office Electronics
- Electronic Test Equipment

General Description

The G751 is a temperature sensor, Delta-Sigma analog-to-digital converter, and digital over-temperature detector with SMBus interface. The host can query the G751 at any time to read temperature. The open-drain Over temperature Shutdown (O.S.) output becomes active when the temperature exceeds a programmable limit. This pin can operate in either "Comparator" or "Interrupt" mode.

The host can program both the temperature alarm threshold (T_{OS}) and the temperature at which the alarm condition goes away (T_{HYST}). In addition, the host can read back the contents of the G751's T_{OS} and T_{HYST} registers. The sensor powers up in Comparator mode with default thresholds of 50°C T_{OS} 45°C T_{HYST} . for G751-1 and 80°C T_{OS} , 75°C T_{HYST} for G751-2.

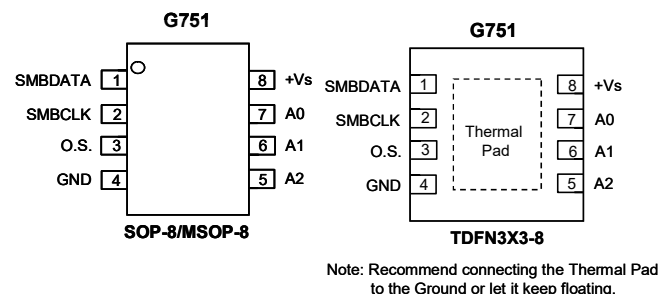
Ordering Information

ORDER NUMBER	T_{OS} T_{HYST}	PACKAGE (Green)
G751-1P1f	50°C/45°C	SOP-8
G751-2P1f	80°C/75°C	SOP-8
G751-2P8f	80°C/75°C	MSOP-8
G751-2RDf	80°C/75°C	TDFN3X3-8

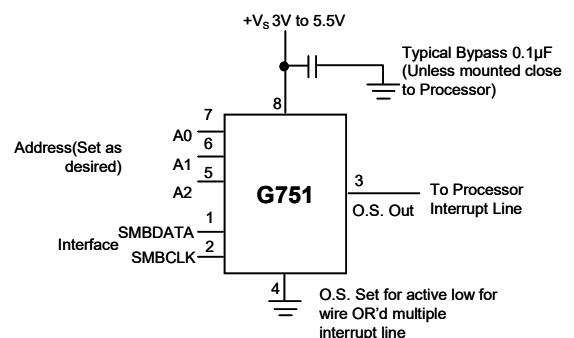
P1: SOP-8; P8: MSOP-8; RD: TDFN3X3-8

Green: Lead Free / Halogen Free.

Pin Configuration



Typical Application Circuit



Absolute Maximum Ratings

Vcc to GND. -0.3V to +6V
 SMBCLK, SMBDAT, O.S., A0, A1, A2 to GND. -0.3V to +6V
 SMBDATA, O.S. Current. -1mA to +50mA
 ESD (HBM). 2000V
 ESD (CDM) 500V
 Thermal Resistance Junction to Ambient ($\theta_{JA}, T_A = +70^\circ\text{C}$)*
 SOP 120°C/W
 MSOP-8/TDFN 140°C/W
 Continuous Power Dissipation ($T_A = +70^\circ\text{C}$)*
 SOP (derate 8.30mW/°C above +70°C) 667mW
 MSOP/TDFN (derate 7.14mW/°C above 70°C) . . . 571mW

Thermal Resistance Junction to Case, (θ_{JC})
 SOP-8. 31°C/W
 MSOP-8. 55°C/W
 TDFN3X3-8 30°C/W
 Operating Temperature Range -55°C to +125°C
 Junction Temperature. +150°C
 Storage temperature Range. -65°C to +165°C
 Reflow Temperature (soldering, 10sec) +260°C

*Please refer to Minimum Footprint PCB Layout Section

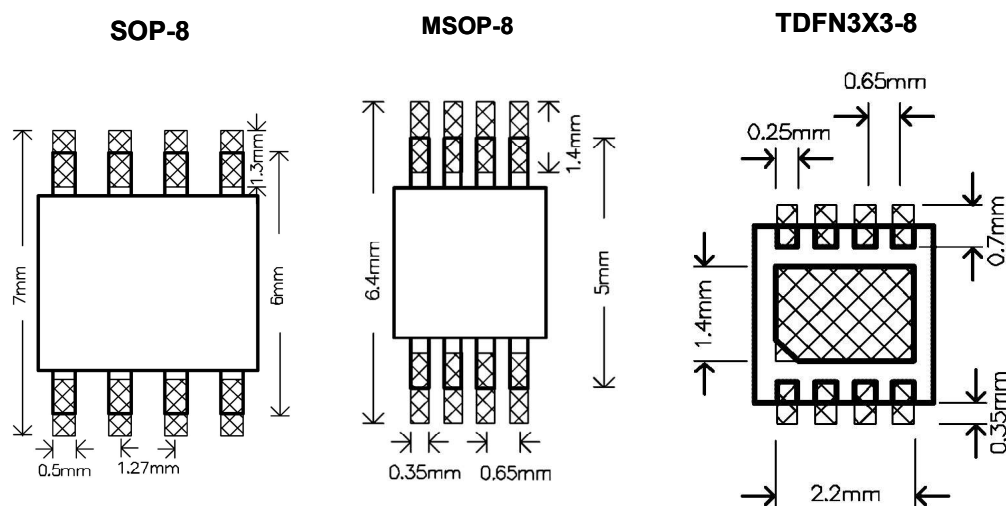
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Temperature-to-Digital Converter Characteristics

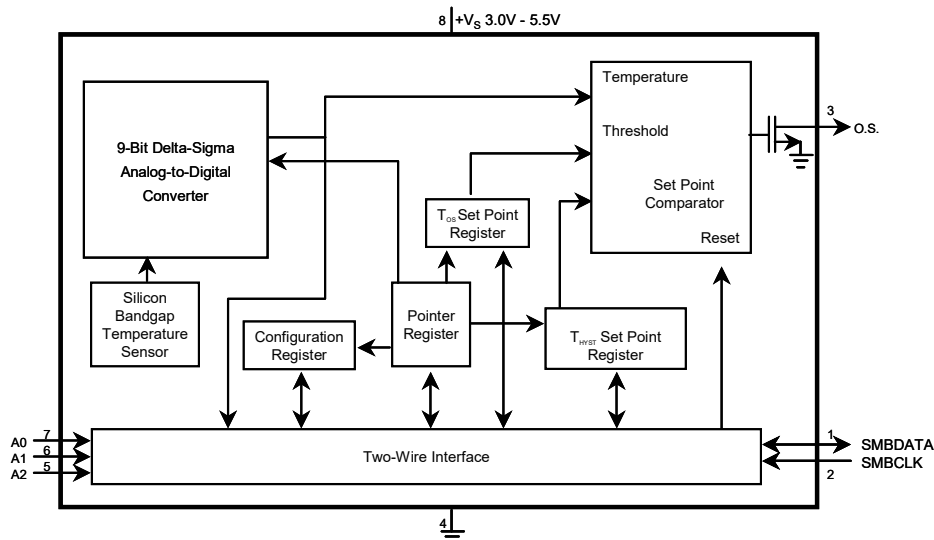
Unless otherwise noted, these specifications apply for $V_S = +3.3\text{V}$ Vdc. **Boldface limits apply for $T_A = T_J = T_{MIN}$ to T_{MAX}** ; all other limits $T_A = T_J = +25^\circ\text{C}$, unless otherwise noted.

PARAMETER	CONDITIONS	TYPICAL	LIMITS	UNITS
Accuracy	$T_A = -25^\circ\text{C}$ to $+100^\circ\text{C}$	---	± 2.0	$^\circ\text{C}$ (max)
	$T_A = -55^\circ\text{C}$ to $+125^\circ\text{C}$	---	± 3.0	
Resolution		9	---	Bits
Temperature Conversion Time		100	---	ms
Quiescent Current	SMBus Inactive	0.25	---	mA
	SMBus Active	---	1.0	mA (max)
	Shutdown Mode	2	---	μA
O.S. Output Saturation Voltage	$I_{OUT} = 4.0\text{mA}$	---	0.8	V (max)
O.S. Delay		---	1	Conversions (min)
		---	6	Conversions (max)
T_{OS} Default Temperature G751-1/G751-2		50/80	---	$^\circ\text{C}$
T_{HYST} Default Temperature G751-1/G751-2		45/75	---	$^\circ\text{C}$

Minimum Footprint PCB Layout Section



Block Diagram



Logic Electrical Characteristics

Digital DC Characteristics

Unless otherwise noted, these specifications apply for $+V_s = +3.3$ Vdc. **Boldface limits apply for $T_A = T_J = T_{MIN}$ to T_{MAX} ; all other limits $T_A = T_J = +25^\circ\text{C}$, unless otherwise noted.**

SYMBOL	PARAMETER	CONDITIONS	TYPICAL	LIMITS	UNITS
$V_{IN(1)}$	Logical "1" Input Voltage		---	$+V_s \times 0.7$ $+V_s + 0.5$	V (min) V (max)
$V_{IN(0)}$	Logical "0" Input Voltage		---	-0.3 $+V_s \times 0.3$	V (min) V (max)
$I_{IN(1)}$	Logical "1" Input Current	$V_{IN} = 5V$	0.005	1.0	μA
$I_{IN(0)}$	Logical "0" Input Current	$V_{IN} = 0V$	-0.005	-1.0	μA
C_{IN}	All Digital Inputs		20	---	pF
I_{OH}	High Level Output Current	$V_{OH} = 5V$	---	100	μA (max)
V_{OL}	Low Level Output Voltage	$I_{OL} = 3\text{mA}$	---	0.4	V (max)
t_{OF}	Output Fall Time	$C_L = 400\text{pF}$, $I_O = 3\text{mA}$	---	250	ns (max)

SMBus Digital Switching Characteristics

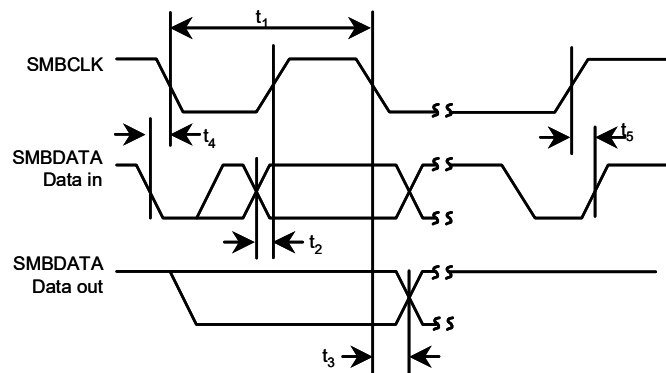
Unless otherwise noted, these specifications apply for $+V_s = +3.3$ Vdc and C_L (load capacitance) on output lines = 80pF unless otherwise specified. **Boldface limits apply for $T_A = T_J = T_{MIN}$ to T_{MAX} ; all other limits $T_A = T_J = +25^\circ\text{C}$, unless otherwise noted.**

The switching characteristics of the G751 fully meet or exceed the published specifications of the SMBus. The following parameters are the timing relationships between SMBCLK and SMBDATA signals related to the G751. They are not the SMBus specifications.

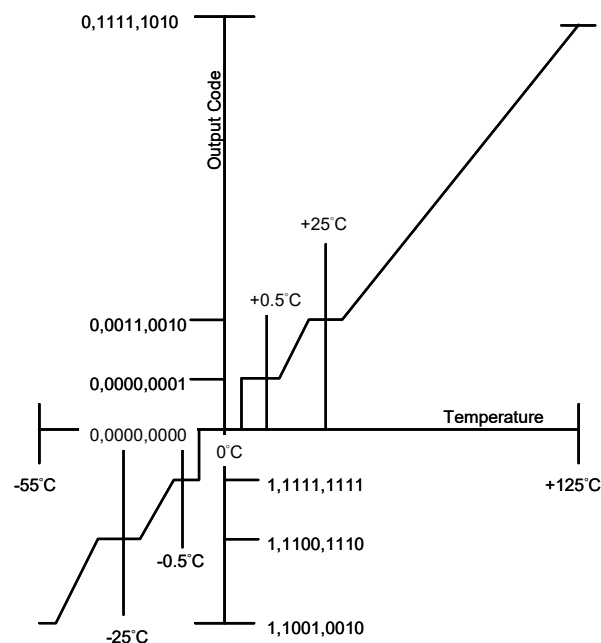
SYMBOL	PARAMETER	CONDITIONS	TYPICAL	LIMITS	UNITS
t_1	SMBCLK (Clock) Period		---	2.5	μs (min)
t_2	Data in Set-Up Time to SMBCLK High		---	100	ns (min)
t_3	Data Out Stable after SMBCLK Low		---	0	ns (min)
t_4	SMBDATA Low Set-Up Time to SMBCLK Low (Start Condition)		---	100	ns (min)
t_5	SMBDATA High Hold Time after SMBCLK High (Stop Condition)		---	100	ns (min)

Pin Description

PIN	LABEL	FUNCTION	TYPICAL CONNECTION
1	SMBDATA	SMBus Serial Bi-Directional Data Line	From Controller
2	SMBCLK	SMBus Clock Input	From Controller
3	O.S.	Overtemperature Shutdown Open Drain Output	Pull Up Resistor, Controller Interrupt Line
4	GND	Power Supply Ground	Ground
5,6,7	A0-A2	User-Set SMBus Address Inputs	Ground (Low, "0") or +Vs (High, "1")
8	+Vs	Positive Supply Voltage Input	DC Voltage from 3V to 5.5V
	Thermal Pad	Recommend connecting the Thermal Pad to the GND or let it keep floating.	



Logic Electrical Characteristics



Temperature-to-Digital Transfer Function (Non-linear scale for clarity)

Functional Description

The G751 temperature sensor incorporates a band-gap type temperature sensor and 9-bit ADC (Delta-Sigma Analog- to-Digital Converter). The temperature data output of the G751 is available at all times via the SMBus. If a conversion is in progress, it will be stopped and restarted after the read. A digital comparator is also incorporated that compares a series of readings, the number of which is user-selectable, to user-programmable setpoint and hysteresis values. The comparator trips the O.S. output line, which is programmable for mode and polarity.

O.S. Output, T_{OS} and T_{HYST} Limits

In Comparator mode the O.S. Output behaves like a thermostat. The output becomes active when temperature exceeds the T_{OS} limit, and leaves the active state when the temperature drops below the T_{HYST} limit. In this mode the O.S. output can be used to turn a cooling fan on, initiate an emergency system shutdown, or reduce system clock speed. Shutdown mode does not reset O.S. state in a comparator mode.

In interrupt mode exceeding T_{OS} also makes O.S. active but O.S. will remain active indefinitely until reset by reading any register via the SMBus interface. Once O.S. has been activated by crossing T_{OS} , then reset, it can be activated again only by temperature going below T_{HYST} . Again, it will remain active indefinitely until being reset by a read. Placing the G751 in shutdown mode also resets the O.S. output.

Default Modes

G751 always powers up in a known state. G751 power up default conditions are:

1. Comparator mode
2. T_{OS} set to 50°C for G751-1, to 80°C for G751-2
3. T_{HYST} set to 45°C for G751-1, to 75°C for G751-2
4. O.S. active low
5. Pointer set to "00"; Temperature Register

With these operating conditions G751 can act as a stand-alone thermostat with the above temperature settings. Connection to an SMBus is not required.

SMBus Interface

The G751 operates as a slave on the SMBus, so the SMBCLK line is an input (no clock is generated by the G751) and the SMBDATA line is a bi-directional serial data path. According to SMBus specifications, the G751 has a 7-bit slave address. The four most significant bits of the slave address are hard wired inside the G751 and are "1001". The three least significant bits of the address are assigned to pins A2-A0, and are set by connecting these pins to ground for a low, (0); or to +Vs for a high, (1).

Therefore, the complete slave address is:

1	0	0	1	A2	A1	A0
MSB				LSB		

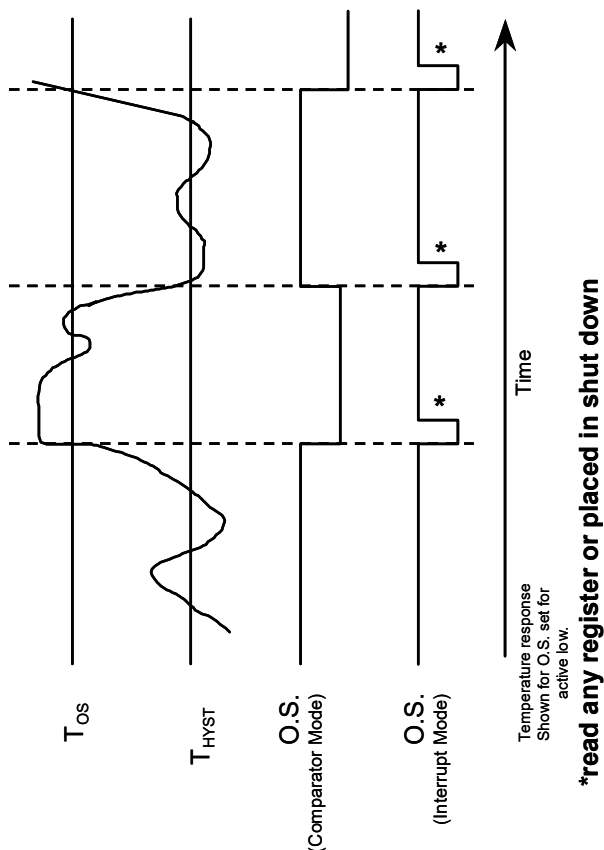


Figure1 O.S. Output Temperature Response Diagram

Temperature Data Format

Temperature data can be read from the Temperature, T_{OS} Set Point, and T_{HYST} Set Point registers; and written to the T_{OS} Set Point, and T_{HYST} Set Point registers. Temperature data is represented by a 9-bit, two's complement word with an LSB (Least Significant Bit) equal to 0.5°C :

TEMPERATURE	DIGITAL OUTPUT	
	BINARY	HEX
+125°C	0 1111 1010	0FAh
+25°C	0 0011 0010	032h
+0.5°C	0 0000 0001	001h
0°C	0 0000 0000	000h
-0.5°C	1 1111 1111	1FFh
-25°C	1 1100 1110	1CEh
-55°C	1 1001 0010	192h

Shutdown Mode

Shutdown mode is enabled by setting the shutdown bit in the Configuration register via the SMBus. Shutdown mode reduces power supply current to $1\mu\text{A}$ typical. In interrupt mode O.S. is reset if previously set and is undefined in Comparator mode during shutdown. The SMBus interface remains active. Activity on the clock and data lines of the SMBus may slightly increase shutdown mode quiescent current. T_{OS} , T_{HYST} , and Configuration registers can be read from and written to in shutdown mode.

Fault Queue

A fault queue of up to 6 faults is provided to prevent false tripping of O.S. when the G751 is used in noisy environments. The number of faults set in the queue must occur consecutively to set the O.S. output.

Comparator/Interrupt Mode

As indicated in the O.S. Output Temperature Response Diagram, Figure 1, the events that trigger O.S. are identical for either Comparator or Interrupt mode. The most important difference is that in Interrupt mode the O.S. will remain set indefinitely once it has been set. To reset O.S. while in Interrupt mode, perform a read from any register in the G751.

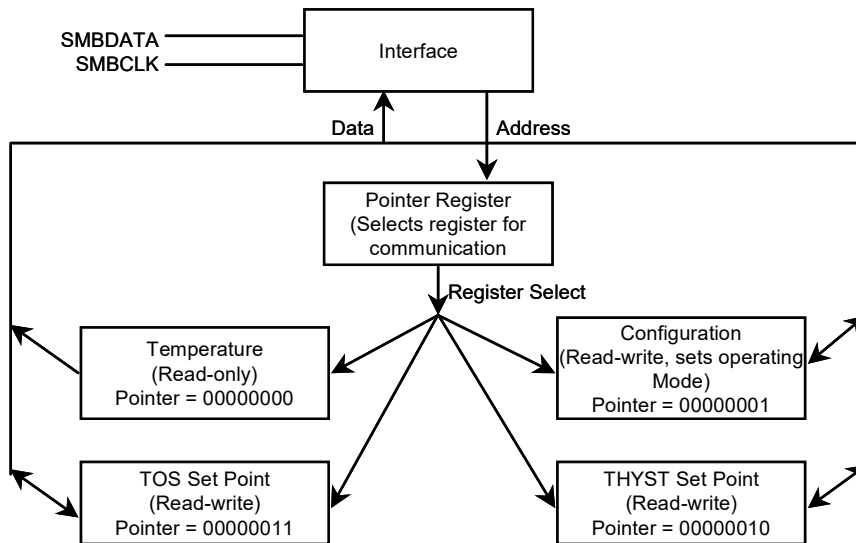
O.S. Output

The O.S. output is an open-drain output and does not have an internal pull-up. A "high" level will not be observed on this pin until pull-up current is provided from some external source, typically a pull-up resistor. Choice of resistor value depends on many system factors but, in general, the pull-up resistor should be as large as possible. This will minimize any errors due to internal heating of the G751. The maximum resistance of the pull up, based on G751 specification for High Level Output Current, to provide a 2V high level, is $30\text{k}\Omega$.

O.S. Polarity

The O.S. output can be programmed via the configuration register to be either active low (default mode), or active high. In active low mode the O.S. output goes low when triggered exactly as shown on the O.S. Output Temperature Response Diagram, Figure 1. Active high simply inverts the polarity of the O.S. output.

Internal Register Structure



There are four data registers in the G751, selected by the Pointer register. At power-up the Pointer is set to "00"; the location for the Temperature Register. The Pointer register latches whatever the last location it was set to. In Interrupt Mode, a read from the G751, or placing the device in shutdown mode, resets the O.S. output. All registers are read and write, except the Temperature register which is read only.

A write to the G751 will always include the address byte and the Pointer byte. A write to the Configuration register requires one data byte, and the T_{OS} and T_{HYST} registers require two data bytes.

Reading the G751 can take place either of two ways: If the location latched in the Pointer is correct (most of the time it is expected that the Pointer will point to Temperature register because it will be the data most frequently read from the G751), then the read can simply consist of an address byte, followed by retrieving the corresponding number of data bytes. If the Pointer needs to be set, than an address byte, pointer byte, repeat start, and another address byte will accomplish a read.

The first data byte is the most significant byte with most signification bit first, permitting only as much data as necessary to be read to determine temperature condition. For instance, if the first four bits of the temperature data indicates an overtemperature condition, the host processor could immediately take action to remedy the excessive temperatures. At the end of a read, the G751 can accept either Acknowledge or No Acknowledge from the Master (No Acknowledge is typically used as a signal for the salve that the Master has read its last byte).

An inadvertent 8-bit read from a 16-bit register, with the D7 bit low, can cause the G751 to stop in a state where the SDA line is held low as shown in Figure 2. This can prevent any further bus communication until at least 9 additional clock cycles have occurred. Alternatively, the master can issue clock cycles until SDA goes high, at which time issuing a "Stop" condition will reset the G751.

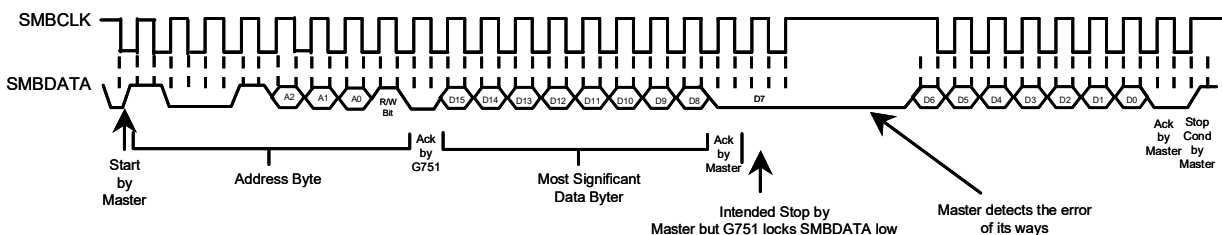


Figure 2. Inadvertent 8-Bit Read from 16-Bit Register where D7 is Zero

Pointer Register

(Selects which registers will be read from or written to):

P7	P6	P5	P4	P3	P2	P1	P0
0	0	0	0	0	0	Register Select	

P0-P1 :Register Select:

P1	P0	REGISTER
0	0	Temperature (Read only) (Power-up default)
0	1	Configuration (Read/Write)
1	0	T _{HYST} (Read/Write)
1	1	T _{OS} (Read/Write)

P2-P7 :Must be kept zero.

Temperature Register

(Read Only):

D15	D14	D13	D12	D11	D10	D9	D8	D7	D6	D5	D4	D3	D2	D1	D0
MSB	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	LSB	X	X	X	X	X	X	X

D0-D6 :Undefined

D7-D15 :Temperature Data. One LSB = 0.5°C. Two's complement format.

Configuration Register

(Read/Write):

D7	D6	D5	D4	D3	D2	D1	D0
0	0	0	Fault Queue		O.S. Polarity	Cmp/Int	Shutdown

Power up default is with all bits "0" (zero).

D0 : Shutdown : When set to 1 the G751 goes to low power shutdown mode.

D1 : Comparator / Interrupt mode : 0 is Comparator mode, 1 is Interrupt mode.

D2 : O.S. Polarity : 0 is active low, 1 is active high. O.S. is an open-drain output under all conditions.

D3-D4 : Fault Queue : Number of faults necessary to detect before setting O.S. output to avoid false tripping due to noise :

D4	D3	NUMBER OF FAULTS
0	0	1 (Power-up default)
0	1	2
1	0	4
1	1	6

D5-D7 : These bits are used for production testing and must be kept zero for normal operation.

T_{HYST} and T_{OS} Register

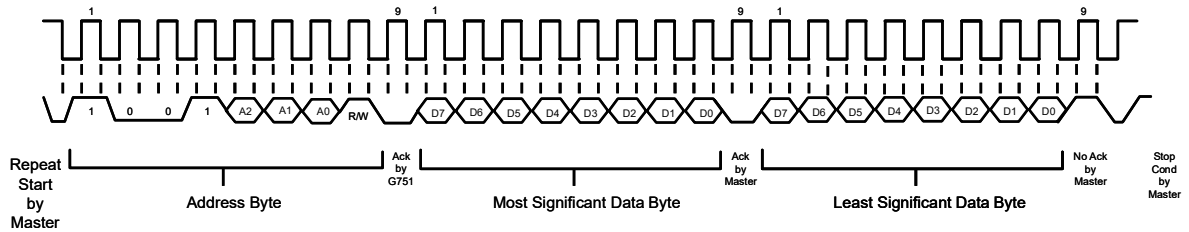
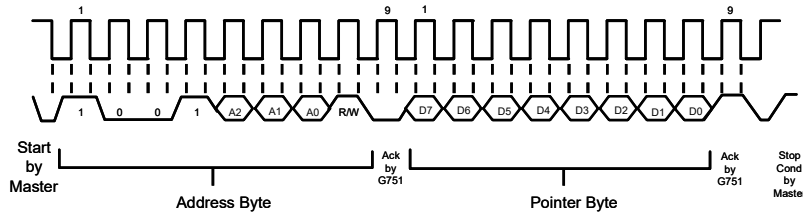
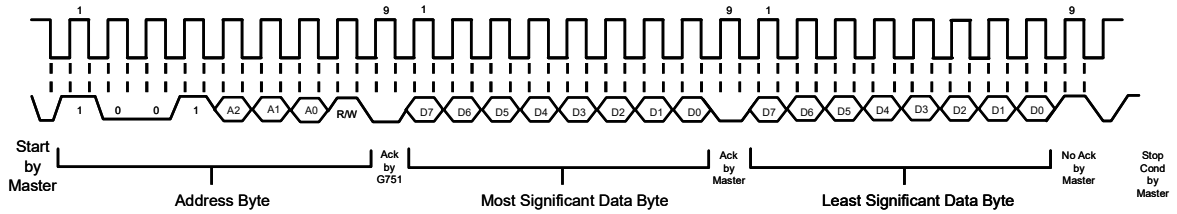
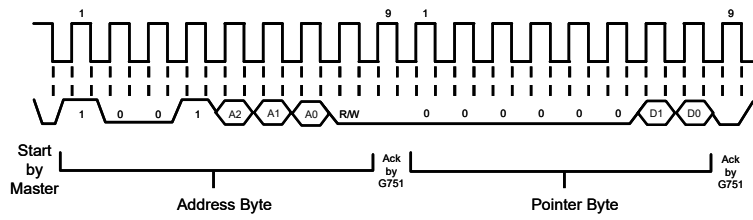
(Read/Write):

D15	D14	D13	D12	D11	D10	D9	D8	D7	D6	D5	D4	D3	D2	D1	D0
MSB	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	LSB	X	X	X	X	X	X	X

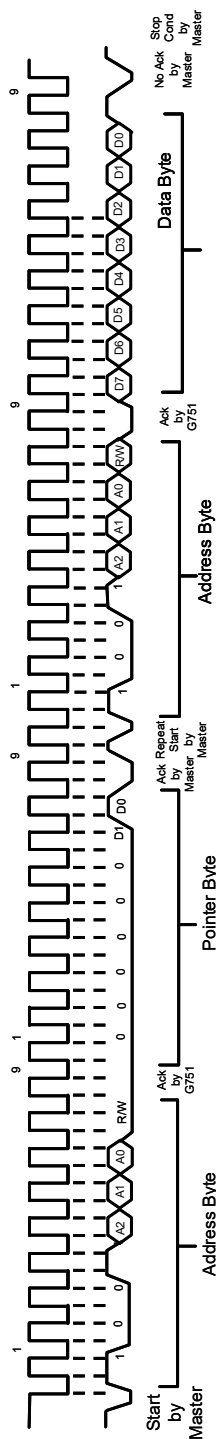
D0-D6 : Undefined

 D7-D15 : T_{HYST} or T_{OS} Trip Temperature Data. Power up default is T_{OS} = 50°C /80°C for G751-1/G751-2, T_{HYST} = 45°C/75°C for G751-1/G751-2.

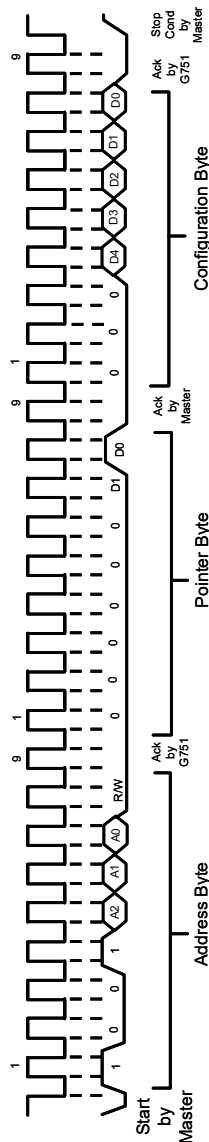
SMBus Timing Diagrams



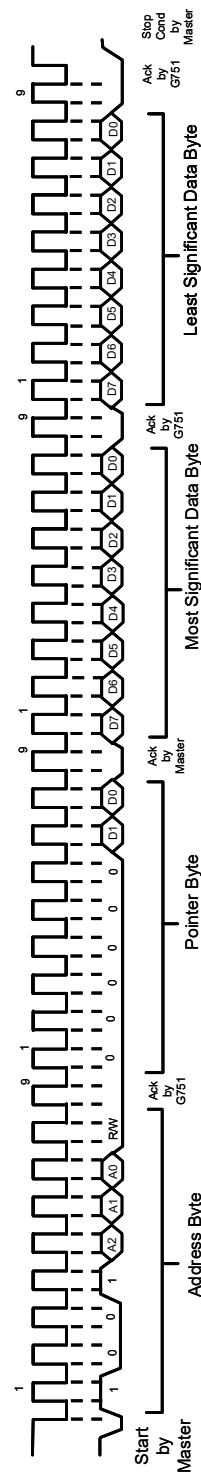
Timing Diagrams (Continued)



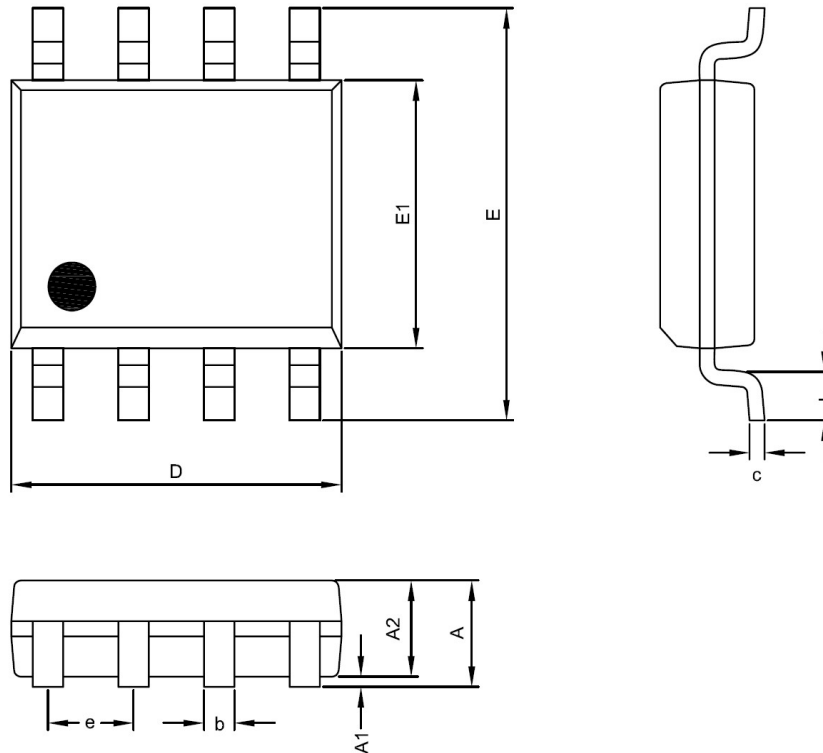
(a) Typical Pointer Set followed by Immediate Read from Configuration Register



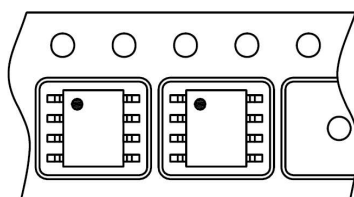
(b) Configuration Register Write



(c) T_{OS} and T_{HYST} Write

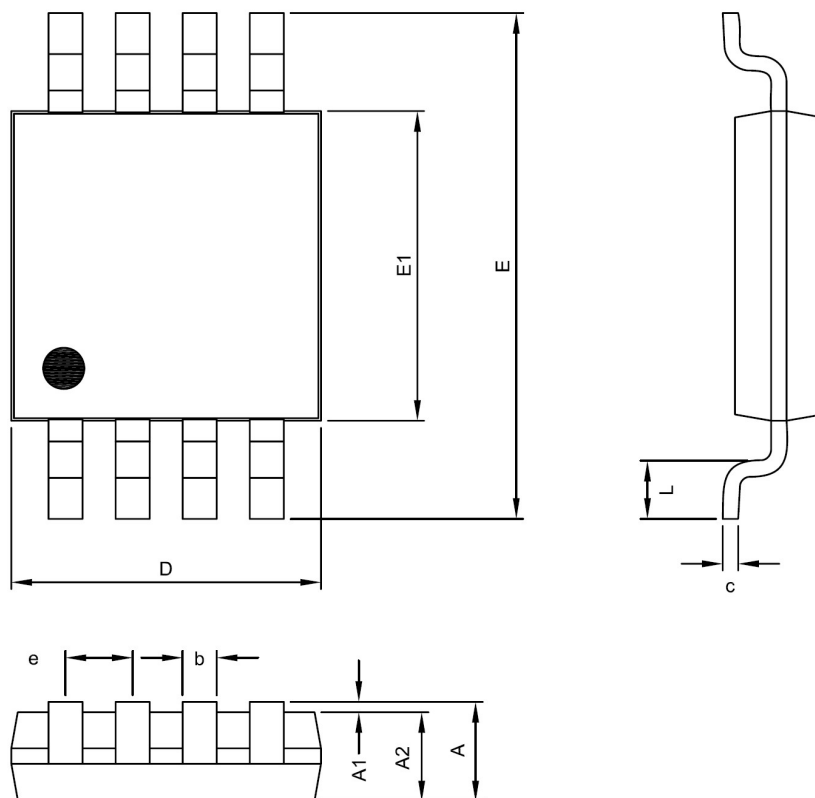
Package Information

SOP-8 (P1) Package

Symbol	DIMENSION IN MM			DIMENSION IN INCH		
	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.
A	1.35	1.55	1.75	0.053	0.061	0.069
A1	0.00	---	0.25	0.000	---	0.010
A2	1.15	1.35	1.50	0.045	0.053	0.059
D	4.80	4.90	5.00	0.189	0.192	0.197
E	5.80	6.00	6.20	0.228	0.236	0.244
E1	3.80	3.90	4.00	0.150	0.153	0.157
c	0.20 REF			0.008 REF		
b	0.33	0.43	0.53	0.013	0.017	0.021
e	1.27 BSC			0.050 BSC		
L	0.40	0.70	1.27	0.016	0.028	0.050

Taping Specification


Feed Direction

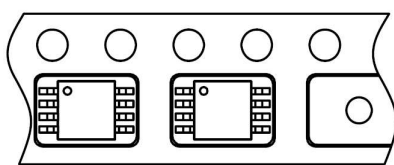
PACKAGE	Q'TY/REEL
SOP-8	2,500 ea



MSOP-8 (P8) Package

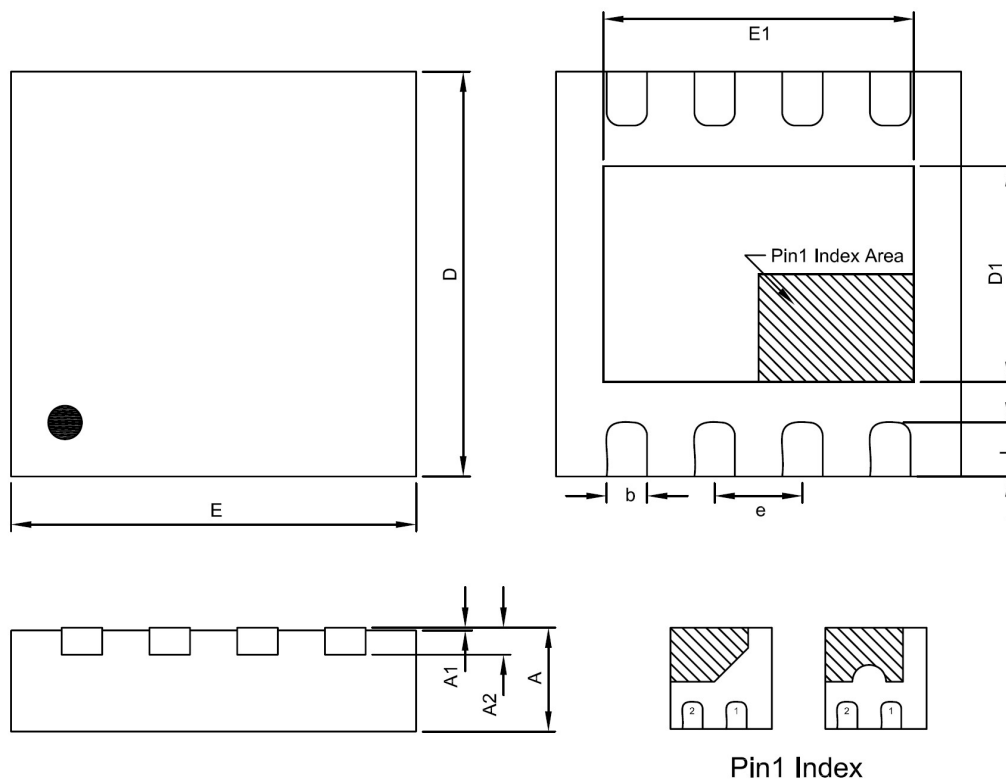
Symbol	DIMENSION IN MM			DIMENSION IN INCH		
	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.
A	0.81	0.95	1.10	0.032	0.037	0.043
A1	0.00	---	0.15	0.000	---	0.006
A2	0.75	0.86	0.96	0.030	0.034	0.038
D	2.85	3.00	3.15	0.112	0.118	0.124
E	4.75	4.90	5.05	0.187	0.193	0.199
E1	2.85	3.00	3.15	0.112	0.118	0.124
b	0.22	0.30	0.38	0.009	0.012	0.015
c	0.15 REF			0.006 REF		
e	0.65 BSC			0.026 BSC		
L	0.4	0.53	0.8	0.016	0.021	0.031

Taping Specification



Feed Direction

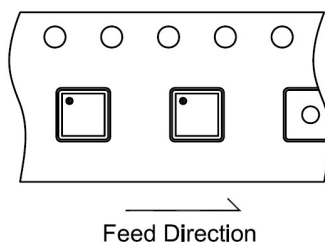
PACKAGE	Q'TY/REEL
MSOP-8	3,000 ea



TDFN3X3-8 Package

Symbol	DIMENSION IN MM			DIMENSION IN INCH		
	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.
A	0.70	0.75	0.80	0.0276	0.0295	0.0315
A1	0.00	---	0.05	0.0000	---	0.0020
A2	0.20 REF			0.0079 REF		
D	2.95	3.00	3.05	0.1161	0.1181	0.1201
E	2.95	3.00	3.05	0.1161	0.1181	0.1201
D1	1.40	1.50	1.60	0.0551	0.0591	0.0630
E1	2.20	2.30	2.40	0.0866	0.0906	0.0945
b	0.20	0.30	0.35	0.0079	0.0118	0.0138
e	0.65 BSC			0.0256 BSC		
L	0.35	0.45	0.525	0.0138	0.0177	0.0207

Taping Specification



PACKAGE	Q'TY/REEL
TDFN3X3-8	3,000 ea

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